INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.2444-01	Div. of Appln. No.	09/077,743
Applicant	Hiroshi TOYODA, et al.		
Filing Date	Herewith 6/23/03	Group:	Not Yet Assigned 22/4

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Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
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A	Wolf, "Silicon Processing for the VLSI Era, Vol. 2 - Process Integration," 1990, Lattice Press, Vol. 2, p. 193				

77.1.	
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*Examiner: Initial if reference through citation is communication to	considered, whether or not citation is in conformance with MPEP 609; draw line f not in conformance and not considered. Include copy of this form with next papplicant.
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